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<u>Verification techniques</u> for cache coherence protocols

Fong Pong, Michel Dubois

March 1997 ACM Computing Surveys (CSUR), Volume 29 Issue 1

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Publisher: ACM Press

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In this article we present a comprehensive survey of various approaches for the verification of cache coherence protocols based on state enumeration, (symbolic model checking, and symbolic state models. Since these techniques search the state space of the protocol exhaustively, the amount of memory required to manipulate that state information and the verification time grow very fast with the number of processors and the complexity of the protocol mechanism ...

Keywords: cache coherence, finite state machine, protocol verification, shared-memory multiprocessors, state representation and expansion

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